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## \*BIBDATASHEET\*

**CONFIRMATION NO. 5161** 

Bib Data Sheet									
SERIAL NUMBE 09/853,005	FILING DATE 05/09/2001 RULE		LASS 711	GROUP A 218			ATTORNEY OCKET NO. JCLA5312		
APPLICANTS									
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** CONTINUING DATA **********************************									
** FOREIGN APPLICATIONS ************************************									
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 07/06/2001									
Foreign Priority claimed 35 USC 119 (a-d) conditi	yes $\square$ no		STATE OR	SHEETS	тот	AL	INDEPENDENT		
35 USC 119 (a-d) conditions by yes no Met after Met Allowance Verified and Acknowledged Examiner's Signature Initials			COUNTRY TAIWAN	DRAWING 3	G CLAI		CLAIMS 3		
ADDRESS J C PATENTS INC 4 Venture Suite 250 Irvine, CA 92618									
TITLE Peripheral device interface chip cache and data synchronization method									
N <sub>0</sub>	FEES: Authority has been given in Paper No to charge/credit DEPOSIT ACCOUNT No for following:				☐ All Fees ☐ 1.16 Fees ( Filing ) ☐ 1.17 Fees ( Processing Ext. of time )				
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